

AMENDMENTS TO THE CLAIMS

Claims 1-6 (Canceled)

Claim 7. (Previously Presented): An intelligent control platform for routing, switching and mixing audio/visual signals, comprising:

a plurality of input ports, each input port configured to define particular ones of a multiplicity of program sources, each input port receiving program signals from a corresponding program source;

a plurality of output ports, each output port configured to define particular ones of a multiplicity of destinations, each output port providing program signals to each corresponding destination;

an adaptively configurable program signal matrix circuit, coupled to receive program signals from each input port and to provide program signals to each output port, the particular input and output ports selected in accordance with a mode select circuit, the mode select circuit adaptively configuring the matrix to pass program signals along a default signal path devised to route the signal solely from the source to the destination while disabling any potential feedback signal paths, and

means for coupling the default signal path through an external mixer, the default signal path configurable to direct the program signals through the mixer in a first operational mode, and to direct signals such that they bypass the mixer in a second operational mode,

wherein the operational modes are selected from the group consisting of digitize, layback, edit and dub modes.

Claim 8 (New): The intelligent control platform according to claim 7, further comprising internal fader controls, the default signal path configurable to direct program signals through the faders in a first operational mode, and to direct signals such that they bypass the faders in a second operational mode.

Claim 9 (New): The intelligent control platform according to claim 8, wherein the program signal matrix circuit comprises a multi-channel crosspoint matrix fabric, the crosspoint matrix fabric including switch energized crosspoint coupling nodes, the switches energized to define signal routing interconnects under software program control of a microprocessor.